



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jaroslav Hynecek

Art Unit: 2811

Serial No.: 10/633,993

Examiner: TBD

Filed: 08/04/03

Docket: TI-36483

For: Clocked Barrier Virtual Phase Charge Coupled Device Image Sensor

LETTER TO THE OFFICIAL DRAFTSPERSON

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz

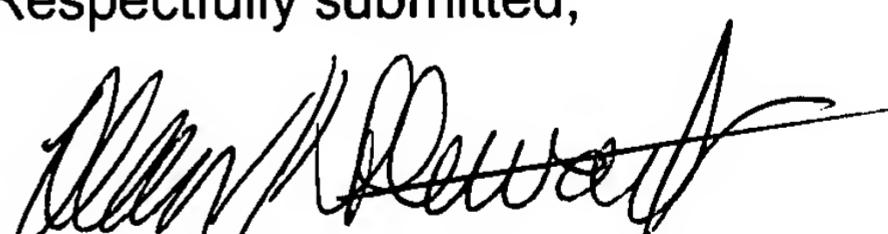
1-6-04
Date

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are **SIX (6)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Respectfully submitted,


Alan K. Stewart
Reg. No. 35,373
Attorney for Applicant

Texas Instruments Incorporated
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